OIPE JC67
JUL 01 2004
PATENT & TRADEMARK OFFICE

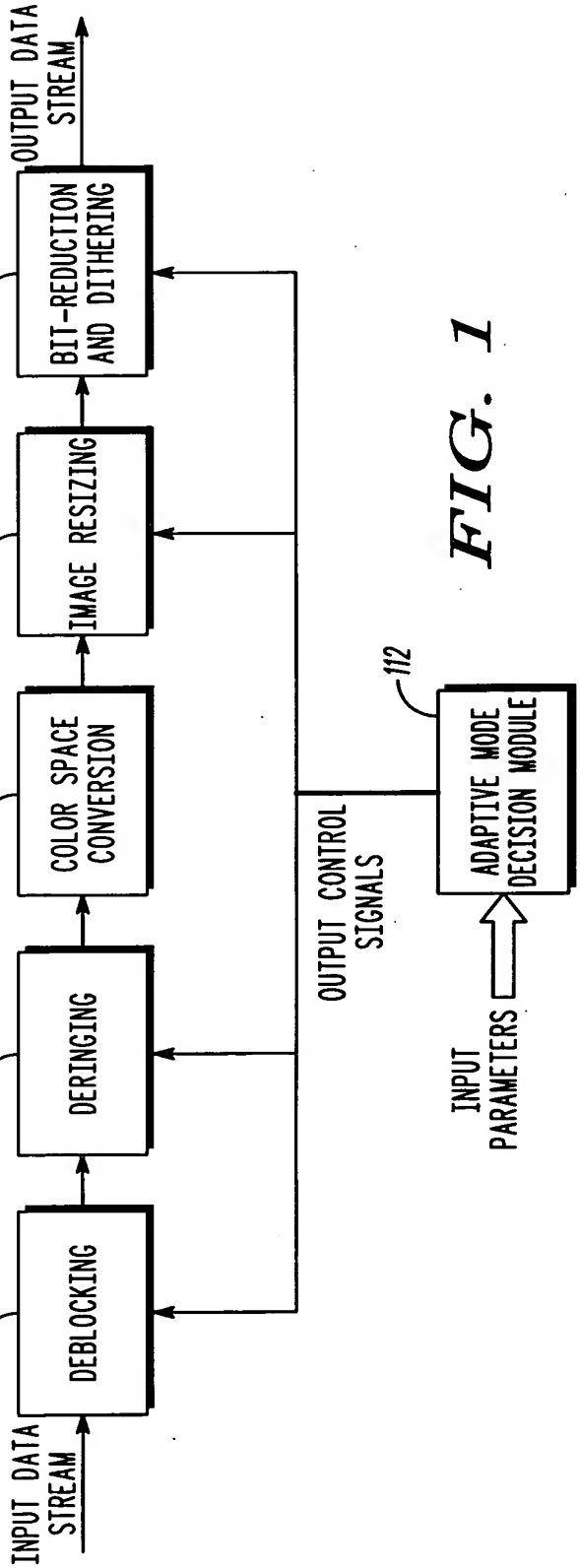

FIG. 1

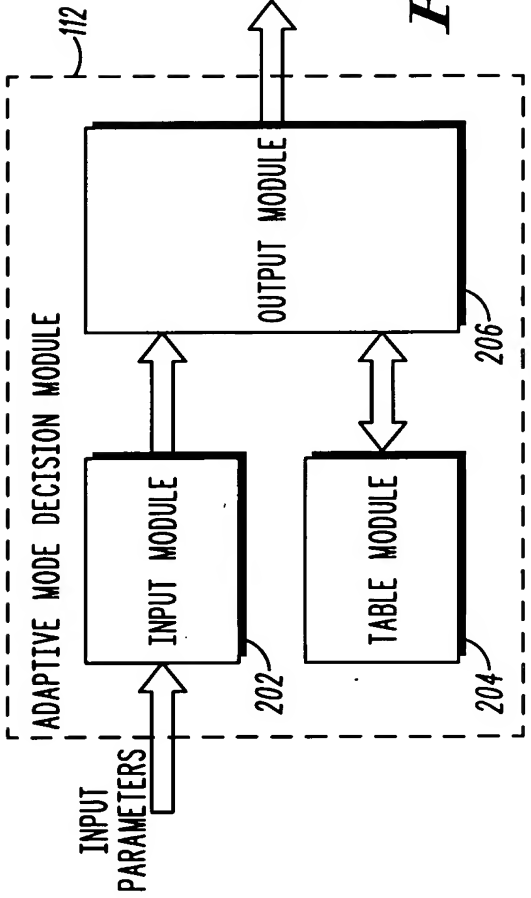

FIG. 2

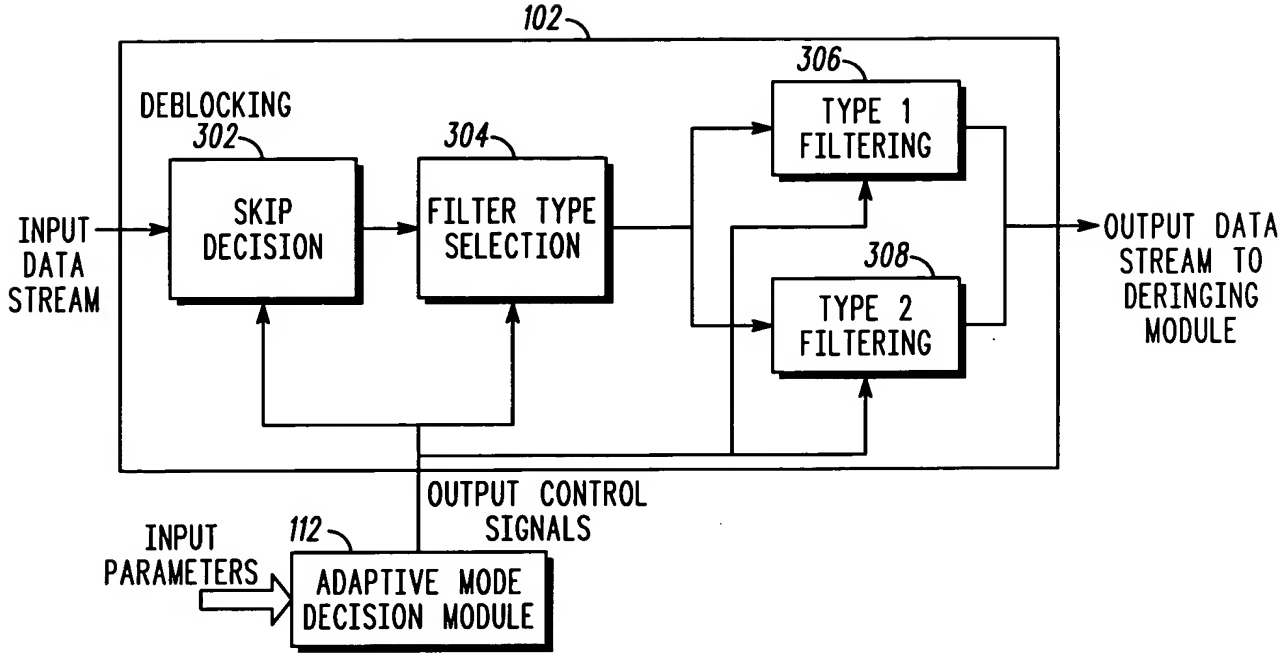
102
DEBLOCKING
302
SKIP DECISION
304
FILTER TYPE SELECTION
306
TYPE 1 FILTERING
308
TYPE 2 FILTERING
INPUT DATA STREAM
OUTPUT DATA STREAM TO DERINGING MODULE
OUTPUT CONTROL SIGNALS
INPUT PARAMETERS
112
ADAPTIVE MODE DECISION MODULE

FIG. 3

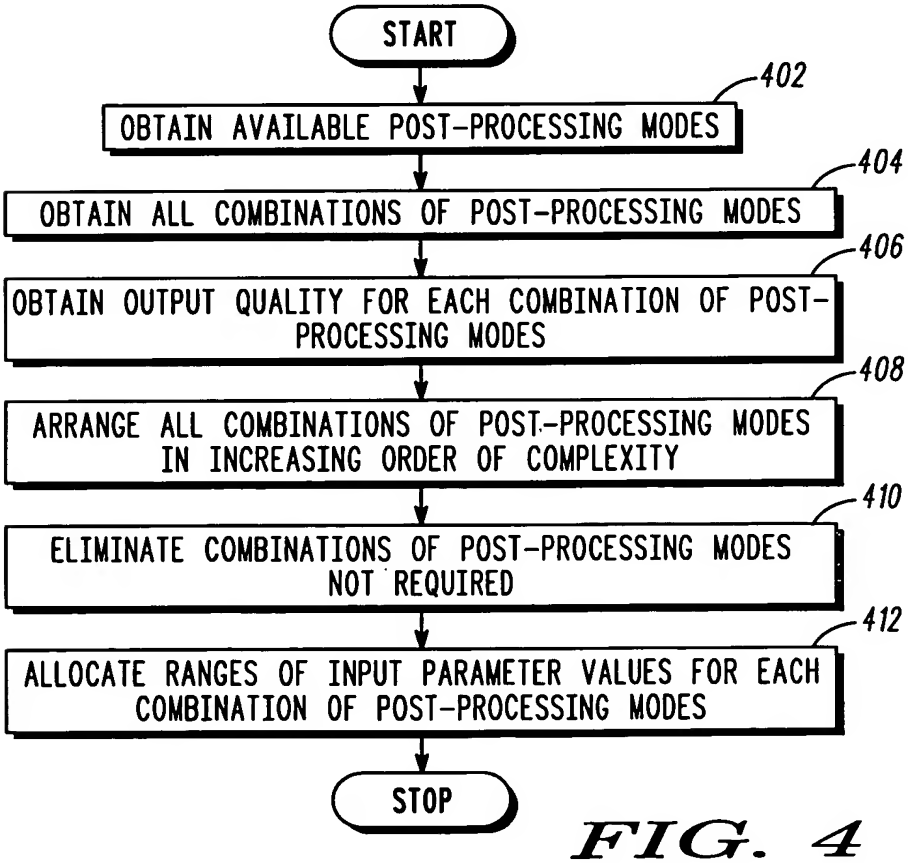
START
402
OBTAIN AVAILABLE POST-PROCESSING MODES
404
OBTAIN ALL COMBINATIONS OF POST-PROCESSING MODES
406
OBTAIN OUTPUT QUALITY FOR EACH COMBINATION OF POST-PROCESSING MODES
408
ARRANGE ALL COMBINATIONS OF POST-PROCESSING MODES IN INCREASING ORDER OF COMPLEXITY
410
ELIMINATE COMBINATIONS OF POST-PROCESSING MODES NOT REQUIRED
412
ALLOCATE RANGES OF INPUT PARAMETER VALUES FOR EACH COMBINATION OF POST-PROCESSING MODES
STOP

FIG. 4

502 REMAINING BATTERY POWER (RBP)	504 SKIP DECISION	506 FILTER TYPE SELECTION	508 TYPE 1 FILTERING	510 TYPE 2 FILTERING	512 DERINGING	514 IMAGE RESIZING	516 BIT REDUCTION AND DITHERING
RBP≥ THR1	ON	HIGH COMPLEXITY	FILTER 1	FILTER 2	HIGH COMPLEXITY	HIGH COMPLEXITY	ON
THR1> RBP≥ THR2	ON	HIGH COMPLEXITY	FILTER 1	FILTER 2	LOW COMPLEXITY	LOW COMPLEXITY	BYPASS
THR2> RBP≥ THR3	ON	LOW COMPLEXITY	FILTER 1A	FILTER 2A	BYPASS	LOW COMPLEXITY	BYPASS
THR3> RBP≥ THR4	ON	BYPASS	FILTER 1A	FILTER 2A	BYPASS	LOW COMPLEXITY	BYPASS
RBP≤ THR4	BYPASS	BYPASS	BYPASS	BYPASS	BYPASS	LOW COMPLEXITY	BYPASS

FIG. 5

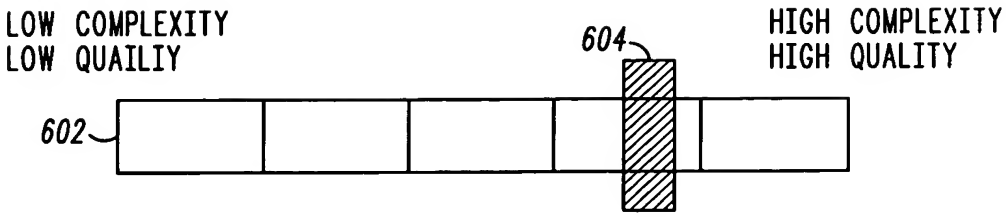
LOW COMPLEXITY
LOW QUAILIY
HIGH COMPLEXITY
HIGH QUALITY
602
604

FIG. 6

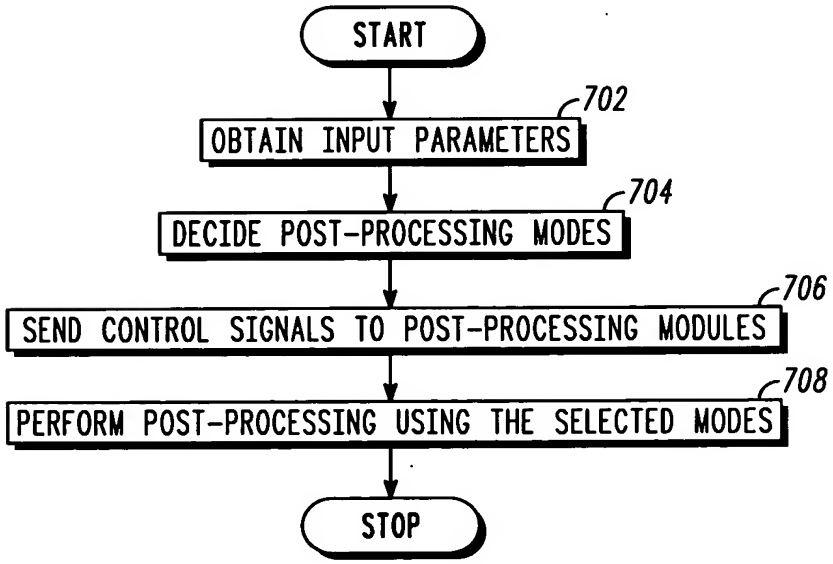
START
OBTAIN INPUT PARAMETERS
702
DECIDE POST-PROCESSING MODES
704
SEND CONTROL SIGNALS TO POST-PROCESSING MODULES
706
PERFORM POST-PROCESSING USING THE SELECTED MODES
708
STOP

FIG. 7